Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	64	memory adj bus$2 with power with (speed clock)	USPAT	OR	OFF	2005/12/05 14:01
S2	44	memory adj bus with first with second with (power speed clock)	USPAT	OR	OFF	2005/11/30 11:27
S3	3	(first adj bus adj speed) same (second adj bus adj speed)	USPAT	OR	OFF	2005/11/30 11:21
S4	12	("5740385").URPN.	USPAT	OR	OFF	2005/11/30 10:31
S5	4	("6763478").URPN.	USPAT	OR	OFF	2005/11/30 11:06
S6	16	("4908523" | "5115503" | "5319772" | "5369771" | "5513340" | "5581693" | "5600839" | "5628019" | "5835750" | "5987614" | "6118306" | "6292430" | "6457135" | "6477609" | "6530001" | "6564332").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/30 11:08
S7	4	(first adj bus adj speed) same (second adj bus adj speed)	US-PGPUB; DERWENT	OR	OFF	2005/11/30 11:27
S8	0	(first adj bus adj speed) same (second adj bus adj speed)	EPO; JPO; IBM_TDB	OR	OFF	2005/11/30 11:27
S9	3	memory adj bus with first with second with (power speed clock)	EPO; JPO; IBM_TDB	OR	OFF	2005/11/30 11:27
S10	40	(memory adj bus$2) with (power near2 (sav$3 conserv$5 consum$5 reduc$5 mode))	USPAT	OR	OFF	2005/12/05 15:37
S11	39	(memory adj bus$2) with (power near2 (sav$3 conserv$5 consum$5 reduc$5 mode))	US-PGPUB	OR	OFF	2005/12/05 14:48
S12	5	(memory adj bus$2) with (power near2 (sav$3 conserv$5 consum$5 reduc$5 mode))	EPO; JPO; IBM_TDB	OR	OFF	2005/12/05 14:48
S13	10	memory adj bus adj speed	USPAT	OR	OFF	2005/12/05 15:56
S14	23	memory adj bus adj speed	US-PGPUB; DERWENT	OR	OFF	2005/12/05 15:56
S15	1	memory adj bus adj speed	EPO; JPO; IBM_TDB	OR	OFF	2005/12/05 15:56
S16	6	ram with rom with (power adj mode)	USPAT	OR	OFF	2005/12/07 09:43
S17	15	(ram adj bus) with (rom adj bus)	USPAT	OR	OFF	2005/12/07 11:08
S18	1	"6694442".pn.	USPAT	OR	OFF	2005/12/07 12:11
S19	0	("6694442").URPN.	USPAT	OR	OFF	2005/12/07 12:05
S20	9	("5218704" | "5490059" | "5727193" | "5754869" | "5925133" | "6367023" | "6528974" | "6574740" | "6608476").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/07 12:05

S21	1	"6336166".pn.	USPAT	OR	OFF	2005/12/07 13:28
S22	0	("6336166").URPN.	USPAT	OR	OFF	2005/12/07 12:11
S23	6	("5317720" | "5388841" | "5579277" | "5719808" | "5784291" | "5936971").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/07 12:11
S24	6	(ram adj bus) and (rom adj bus) and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	USPAT	OR	OFF	2005/12/07 14:15
S25	4	(ram adj bus) and (rom adj bus) and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/07 13:33
S26	13	(first adj memory adj bus) and (second adj memory adj bus) and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	USPAT	OR	OFF	2005/12/07 14:01
S27	9	(first adj memory adj bus) and (second adj memory adj bus) and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	US-PGPUB; DERWENT	OR	OFF	2005/12/07 14:00
S28	0	(first adj memory adj bus) and (second adj memory adj bus) and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	EPO; JPO; IBM_TDB	OR	OFF	2005/12/07 14:00
S29	1	(first adj memory adj bus) with (second adj memory adj bus) and hewlett.as.	USPAT	OR	OFF	2005/12/07 14:02
S30	0	(ram adj bus) with (rom adj bus) and hewlett.as.	USPAT	OR	OFF	2005/12/07 14:02
S31	0	(ram adj bus) and (rom adj bus) and hewlett.as.	USPAT	OR	OFF	2005/12/07 14:02
S32	15	(ram adj bus) same (rom adj bus)	USPAT	OR	OFF	2005/12/07 14:06
S33	2	(ram adj bus) same (rom adj bus)	EPO; JPO; IBM_TDB	OR	OFF	2005/12/07 14:06
S34	17	(ram adj bus) and (rom adj bus) and (speed frequency)	USPAT	OR	OFF	2005/12/07 14:17
S35	3121	(first adj bus) and (second adj bus) and (speed frequency)	USPAT	OR	OFF	2005/12/07 14:17
S36	288	(first adj bus) same (second adj bus) same (speed frequency)	USPAT	OR	OFF	2005/12/07 14:18
S37	34	(first adj bus) same (second adj bus) same (speed frequency) same power	USPAT	OR	OFF	2005/12/07 14:38
S38	236	((control adj system) controller) near3 ((first ram) adj bus and (second rom) adj bus)	USPAT	OR	OFF	2005/12/07 14:39

S39	109	((control adj system) controller) near3 ((first ram) adj bus with (second rom) adj bus)	USPAT	OR	OFF	2005/12/07 17:35
S40	13	((control adj system) controller) near3 ((first ram) adj bus with (second rom) adj bus)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/07 15:55
S41	88	ram with rom with (separate independent) with bus$2	USPAT	OR	OFF	2005/12/07 17:38
S42	1	ram with rom with (separate independent) with bus$2 with (clock speed)	USPAT	OR	OFF	2005/12/07 17:37
S43	0	ram with rom with (separate independent) with bus$2 with power	USPAT	OR	OFF	2005/12/07 17:37
S44	2	ram with rom with (separate independent) with bus$2 same power	USPAT	OR	OFF	2005/12/07 17:37
S45	25	ram with rom with (separate independent) with bus$2 and (power near2 (sav$3 conserv$5 consum$5 reduc$5))	USPAT	OR	OFF	2005/12/07 17:38